CLAIMS

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1	1.	A syst	em for s	ecure da	ta communication, the system comprising:	
2		a.	a proc	essor tha	t provides a first virtual address, a second virtual address, and a	
3	process identif	fier;				
4		b.	a first	memory	circuit coupled to the processor, the first virtual address	
5	corresponding	to a firs	t physica	al addres	s of the first memory;	
6		c.	a men	nory man	agement circuit coupled to the processor, the memory management	
7	circuit compri	sing a se	cond me	emory ci	rcuit in operation containing indicia of:	
8			(1)	a first	association of the first virtual address, the first physical address, and	
9	the process ide	entifier;	and			
0			(2)	a seco	nd association of the second virtual address, the second physical	
1	address, and the process identifier; wherein					
2			(3)	the me	emory management circuit provides the first physical address in	
.3	response to re-	ceiving	the first	physical	address and the process identifier, and provides the second physical	
4	address in resp	ponse to	receivin	g the sec	cond physical address and the process identifier; and	
.5		d.	a netv	vork inte	rface comprising:	
.6			(1)	a third	memory circuit in operation containing indicia of:	
7				(a)	a third association of the first virtual address and the first physical	
8	address; and					
9				(b)	a fourth association of the second physical address and the first	
20	physical addre	ess;				
21			(2)	a com	mand interface circuit that provides a signal in response to receiving	
22	the second ph	ysical ac	dress ar	nd the fir	st virtual address; and	
23			(3)	a brid	ge for coupling the system to a computer network for data	
24	communication	on, the b	ridge cir	cuit com	prising an interface circuit that couples the system to a provided	
25	network for d	ata com	municati	on, the b	oridge circuit operative, in response to the signal:	
26				(a)	to obtain the first physical address from the third memory circuit as	
27	addressed in a	accordar	ice with	the first	virtual address; and	
28				(b)	to transfer data between the interface circuit and the first memory	
29	circuit as add	ressed b	y the firs	st physic	al address for data communication.	

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1	2.	A method for data communication, the method performed by a first computer for					
2	communication	with a second computer, the method comprising:					
3		creating a password;					
4		establishing a data communication channel with the second computer, the channel being					
5	identified by a	channel identifier;					
6		associating the password with the channel identifier;					
7		creating a first map that associates a plurality of virtual I/O addresses with a plurality of					
8	physical I/O ad	dresses;					
9		associating the first map with a process, the process identified by a process identifier;					
0	requiring the process identifier for accessing the first map;						
1	creating a second map that associates a plurality of virtual memory addresses with a						
2	plurality of phy	sical memory addresses;					
3		determining a memory handle in accordance with a virtual address of the second map;					
4		associating the password and the memory handle with the second map;					
5		requiring the password for accessing the second map; and					
6		communicating via the channel data identified in accordance with the memory handle.					
1	3.	A method for transmitting data onto a network, the method comprising:					
2		providing a memory handle corresponding to a registered virtual memory address, data at					
3	the virtual men	nory address for transmission onto the network;					
4		issuing a command with reference to a registered virtual I/O address;					
5		determining, in response to the command, a physical memory address in accordance with					
6	the memory ha	ndle; and					
7		transmitting data that was read in accordance with the physical memory address.					
1	4.	A method for data communication, the method comprising:					
2		performing, by a central processor, an I/O write instruction for effecting data					
3	communication	n by a network controller, the I/O write instruction associated with a process identifier, the					
4	instruction comprising a registered doorbell virtual page number, an operation identifier, and a registered						
5	virtual memory address, each registered address being associated with the process identifier;						

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6		permitting conversion of the doorbell virtual page number to a doorbell physical page							
7	number in accordance with the process identifier;								
8	associating the registered virtual memory address, the doorbell physical page number, and								
9	password;								
10		permitting conversion of the registered virtual memory address to a physical memory							
11	address in acc	cordance with the password; and							
12		performing data communication as effected by the I/O write instruction in accordance with							
13	data read in accordance with the physical memory address.								
1	5.	A method for data communication, the method performed by a network interface of a first							
2	computer, the	first computer comprising a first memory, the network interface comprising a second							
*	memory, the method comprising:								
4	•	receiving a channel identifier;							
ម		obtaining from a first data structure of the second memory a first password and a physical							
2 3 4 5 6	address of a description of a block to send, the first data structure accessible in accordance with the channel								
₌ 7	identifier;								
8		obtaining from a second data structure of the first memory a memory handle and a first							
8 9	virtual addres	ss referring to the first memory, the second data structure accessible in accordance with the							
10	physical address of the description of the block to send;								
11		determining an index value in accordance with the memory handle and the first virtual							
12	address;								
13		obtaining from a third data structure of the second memory a second password and a first							
14	physical addr	address corresponding to the first virtual address, the third data structure accessible in accordance							
15	with the index value;								
16		abandoning data communication if the first password does not compare successfully with							
17	the second password; and								
18		engaging in data communication with reference to the first physical address.							
1	6.	A data structure maintained in a network interface, the network interface for installation in a							
2	host compute	er, the data structure comprising:							
2		a plurality of entries, each entry comprising.							

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4			(1)	a physical address of a page of memory of the host computer;		
5			(2)	a password; and		
6			(3)	a validity flag; wherein		
7		b.	entrie	s in the data structure are addressable by an index value, the index value being		
8	determined in	accorda	ance with a sum of a memory handle and a virtual address of a page of memory of the			
9	host computer, the virtual address corresponding to the physical address.					
1	7.	A dat	a structu	are maintained in a network interface, the network interface for installation in a		
2	host computer, the network interface for data communication via a plurality of channels, the data structure					
3	comprising:					
= 4		a.	a plui	rality of entries, each entry comprising:		
4 5			(1)	a first physical I/O address of the host computer, the first physical I/O		
6	address for addressing the network interface;					
lim			(2)	a password; and		
= 8			(3)	a second physical memory address of the host computer, the second		
<u> </u>	physical for identifying data for communication by the network interface; wherein					
	• •	b.	entrie	es in the data structure are addressable by a channel identifier, the channel		
10 11 	identifier for id			ticular channel of the plurality of channels for data communication.		
int.						